

Type	L #	Hits	Search Text	Dbs	Time Stamp	Comments	Error Definition	Errors
1	IS&R L1	2035	((385/88) or (385/89) or (385/90) or (385/91) or (385/92) or (385/93) or (385/94)).CCLS.	USPAT; EPO; JPO	2002/ 08/13 12:53			0
2	BRS L2	2419 18	(printed adj2 (circuit or circuits or board or boards or card or cards)) or (circuit adj2 (board or boards or card or cards or substrate or substrates)) or pcb or pcbs	USPAT; EPO; JPO	2002/ 08/13 13:09			0
3	BRS L3	1157 67	(electromagnetic or em or emi or rf or rfi or interference) with (shield or shields or shielded or shielding or reduce or reduced or reduces or reducing or reduction or prevent or prevents or prevented or preventing or prevention)	USPAT; EPO; JPO	2002/ 08/13 13:11			0
4	BRS L4	6100	2 with (perpendicular or perpendicularly or orthogonal or orthogonally)	USPAT; EPO; JPO	2002/ 08/13 13:11			0
5	BRS L5	75	1 and 2 and 3	USPAT; EPO; JPO	2002/ 08/13 13:12			0
6	BRS L6	48	1 and 4	USPAT; EPO; JPO	2002/ 08/13 13:12			0
7	BRS L7	106	5 or 6	USPAT; EPO; JPO	2002/ 08/13 14:47			0

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